

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Logan, et al

Examiner: Dolan, Jennifer M.

Serial No.:

Art Group Unit: 2813

Filed: Herewith

Title: METHOD AND STRUCTURE TO REDUCE CMOS INTER-WELL LEAKAGE

Commissioner for Patents  
Alexandria, VA 22313-1450

**PRELIMINARY AMENDMENT**

Sir:

In connection with filing the above-identified application, Applicants submit the following amendments and remarks for consideration by the Examiner and entry of record in the above-identified patent application.

**AMENDMENT TO THE SPECIFICATION**

On page 1 at the first line following the title, please insert the following section:

**Reference to Related Application**

This application is a divisional of Application No.09/803,117, filed March 10, 2001, now issued as U.S. Patent No. (number not yet assigned).

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Dated:

*Maryann Luisi*  
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